

A GPU accelerated method for 3-D nonlinear Kelvin ship wave patterns simulation

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Abstract: Ship wave is of great interest for wave drag, coastal erosion and ship detection. In this paper, a highly-paralleled numerical scheme is proposed for simulating three-dimensional (3-D) nonlinear Kelvin ship waves effectively. First, a numerical model for nonlinear ship waves is established based on potential flow theory, boundary integral method and Jacobian-free Newton-Krylov (JFNK) method. In order to improve computational efficiency and reduce data storage of JFNK method, a banded preconditioner method is then developed by formulating the optimal bandwidth selection rule. After that, a Graphics Process Unit (GPU) based parallel computing framework is designed, and a GPU solver is developed by using Compute Unified Device Architecture (CUDA) language. Finally, numerical simulations of 3-D nonlinear ship waves under multiple scales are performed by using the GPU and CPU solvers. Simulation results show that the proposed GPU solver is more efficient than the CPU solver with the same accuracy. More than 66% GPU memory can be saved and the computational speed can be accelerated up to 20 times. Hence, the computation time for Kelvin ship waves simulation can be significantly reduced by applying the GPU parallel numerical scheme, which lays a solid foundation for practical ocean engineering.

Keywords: Kelvin wake pattern; GPU acceleration; Boundary integral method; JFNK method; Banded preconditioner method

1. Introduction

This study is dedicated to studying highly-parallel algorithms for steady three-dimensional free surface profiles that are caused by a disturbance to a free stream. These profiles will appear stationary in the reference frame of the moving ship, referred to as “Kelvin ship waves” [1]. Researches on Kelvin ship wave patterns have ongoing practical applications to ship hull design, ship detection and environmentally friendly shipping policies [2–6].

Froude [7], a famous naval architect, first comprehensively described the morphology and main characteristics of ship waves. Under the assumption of infinite water depth, Kelvin [8] replaced a moving ship with a pressure disturbance point moving in a constant velocity straight line on the water surface, proposed the famous Kelvin angle of 19.47° . In recent years, with the further study of ship wave characteristics, Rabaud [9] noted that for sufficiently fast-moving ships, contrary to commonly held views, the wake angle that is observed behind a steadily moving ship is less than the well-known Kelvin angle. This finding aroused the interest of many academics. Subsequently, various effect factors for the Kelvin wake form were discussed in plenty of papers, e.g., Froude number [10,11], non-axisymmetric simplified ship models and interference effects [12–16], shear current, submergence depth and finite depth [17–20], surface tension and the bottom topography [21, 22], and viscosity [23], etc. Accordingly, the research method of ship waves has gradually shifted from the previous analytical algorithms to numerical simulation.

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The mathematical analysis of ship wave patterns has a very long history, the overwhelming majority of which concerns linear theories. Havelock [24] firstly presented the linear solution for the classic problem of flow past a pressure distribution. Such ideal perturbations can also be replaced by a single submerged point source singularity [25] and submerged bodies [26,27]. Another approach is to consider the ship wave pattern due to a thin ship [28,29]. With the development of numerical methods and computer technology, numerical simulation methods become more and more popular, and the study of ship waves had been extended from two-dimensional (2-D) linear problems to three-dimensional (3-D) nonlinear problems. Nowadays, there are three numerical methods widely used to solve surface wave problems: finite-difference method, finite-element method and boundary integral method [30–36]. In particular, Forbes [37,38] apply boundary integral method to build a series of integro-differential equation by placing a mesh of $N \times M$ grid points over the truncated plane, the full nonlinear free surface flow problem was solved with moderate efficiencies. In more recent times, many papers have applied Forbes's formulation to solve fully 3-D nonlinear ship waves with meshes between 60×20 and 181×61 [39–41]. And Pethiyagoda [42] noted that with less than 100 points used along the x -direction, the resolution over each wavelength is simply not of a sufficient standard for any claims about grid-independence to be made.

With increasing mesh size, however, the computation time increases exponentially using only Central Processing Unit (CPU) computation power. Therefore, people are seeking a viable, cheap and portable alternative approach. As the electric industry develops, the Graphics Processing Unit (GPU), a multi-processor designed to optimize for the execution of a massive number of threads, has become an alternative acceleration approach. Currently, the powerful GPU parallel computing ability has been used to improve the studies on ocean engineering. Hérault [43] introduced the GPU acceleration technique in Smoothed Particle Hydrodynamics (SPH) to simulate the dam-break flow. Hori [44] developed a GPU-based MPS code and achieved 7 times speedup in simulating 2-D dam-break flow with 110 thousand particles. Pethiyagoda [42] introduced the GPU acceleration technique in the boundary integral method to improve the computational efficiency of 3-D nonlinear ship wave problem. Xie [45] employed an in-house solver MPSGPU-SJTU coupled with GPU techniques for the liquid sloshing simulation, to study the factors leading to the 3-D effect. LU [46] developed a GPU-accelerated high-order spectral solver to simulate the wave propagation and interaction with current in far field.

In this paper, a parallel solution framework based on GPU is designed for nonlinear ship wave problem, in which almost all operations are performed in GPU device. Since the nonlinear boundary integral equation on each node is independent of the synchronous equations on other nodes, plenty of threads on GPU can be used to complete the integration operation for each node simultaneously. In addition, the parallel computing method can be used for the calculation of the large-scale linear sparse system, the complex inversion process is quickly finished by using Compute Unified Architecture (CUDA) language. According to this framework, a highly-paralleled GPU solver is proposed to simulate 3-D nonlinear Kelvin ship waves. The computation speed for the 3-D nonlinear ship waves simulation can be significantly increased, it is convenient to study the larger scale problems. On the other hand, the size of Random-Access Memory limits grid growth, the application of the banded preconditioner method can greatly save running memory to break through this limitation. The banded preconditioner method helps to achieve the standard for the grid-independence.

The remainder of this paper is organized as follows. A brief introduction of the problem formulation is given in Section 2. In Section 3, the banded preconditioner JFNK algorithm is described. In Section 4, the theory and implementation of the GPU acceleration technique are presented. The accuracy, efficiency and capability of the GPU solver are verified in Section 5, and a summary in Section 6 concludes the paper.

2. Numerical Model

This paper supposes that a flow is directed along the positive x -axis with uniform speed U . Considering the irrotational flow of an inviscid, incompressible fluid of infinite depth with ignoring the effects of surface tension, the potential flow theory is applied. Therefore, a source singularity of strength m is introduced at a distance L below the surface, as illustrated in Figure 1. The transient waves will be generated on the free surface, on account of the disturbance caused by a source. Free surface wave height and flow field velocity potential can be expressed as $z = \zeta(x, y)$ and $\Phi(x, y, z)$. This paper is devoted to the steady-state problem that arises in the long time limit of this flow.

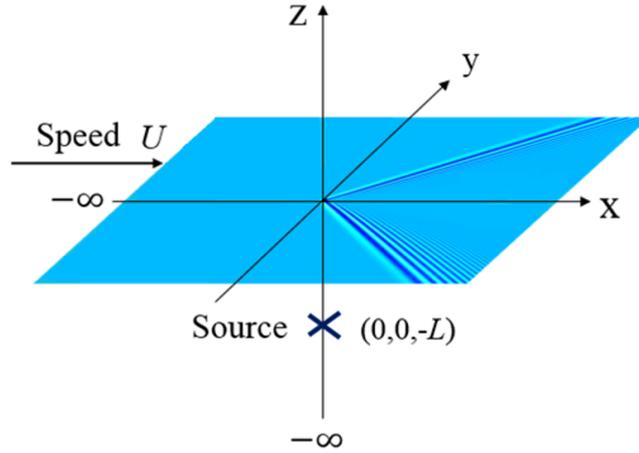


Figure 1. Illustration of the disposition of the fluid.

Dimensionless analysis is performed with fluid velocity U and distance L . The dimensionless velocity potential $\Phi'(x', y', z')$ satisfies Laplace's equation, the free surface kinematic and dynamic boundary condition, the radiation condition and the limiting behavior of source singularity. Then the problem is solved by the boundary integral method, the velocity potential can be regarded as a function simply of the two independent variables x' and y' , and will be denoted by the symbol $\phi'(x', y') = \Phi'(x', y', \zeta'(x', y'))$. The final boundary integral equation is described, as follows:

$$2\pi(\phi'(Q) - x') = \int_0^\infty \int_{-\infty}^\infty [\phi'(P) - \phi'(Q) - \rho' + x'] K^{(1)}(\rho', \sigma', x', y') d\sigma' d\rho' + \int_0^\infty \int_{-\infty}^\infty \zeta'_\rho(P) K^{(2)}(\rho', \sigma', x', y') d\sigma' d\rho' - \frac{\epsilon'}{[x'^2 + x'^2 + (\zeta'(Q) + 1)^2]^{\frac{1}{2}}} \quad (1)$$

where the kernel functions $K^{(1)}(\rho', \sigma', x', y')$ and $K^{(2)}(\rho', \sigma', x', y')$ are described, as follows:

$$K^{(1)}(\rho', \sigma', x', y') = \frac{\zeta'(P) - \zeta'(Q) - (\rho' - x')\zeta'_\rho(P) - (\sigma' + y')\zeta'_\sigma(P)}{\left[(\rho' - x')^2 + (\sigma' + y')^2 + (\zeta'(P) - \zeta'(Q))^2 \right]^{\frac{3}{2}}} + \frac{\zeta'(P) - \zeta'(Q) - (\rho' - x')\zeta'_\rho(P) - (\sigma' - y')\zeta'_\sigma(P)}{\left[(\rho' - x')^2 + (\sigma' - y')^2 + (\zeta'(P) - \zeta'(Q))^2 \right]^{\frac{3}{2}}}$$

$$K^{(2)}(\rho', \sigma', x', y') = \frac{1}{\left[(\rho' - x')^2 + (\sigma' + y')^2 + (\zeta'(P) - \zeta'(Q))^2 \right]^{\frac{3}{2}}} + \frac{1}{\left[(\rho' - x')^2 + (\sigma' - y')^2 + (\zeta'(P) - \zeta'(Q))^2 \right]^{\frac{3}{2}}}$$

There is a singularity in the second integral of boundary integral equation Eq. (1), it can be solved as follows:

$$\int_{y'_1}^{y'_M} \int_{x'_1}^{x'_N} [\zeta'_\rho(P) K^{(2)}(\rho', \sigma', x', y') - \zeta'_x(Q) S^{(2)}(\rho', \sigma', x', y')] d\sigma' d\rho' + \zeta'_x(Q) I'(Q) \quad (2)$$

and

$$I'(Q) = \int_{y'_1}^{y'_M} \int_{x'_1}^{x'_N} S^{(2)}(\rho', \sigma', x', y') d\sigma' d\rho' \quad (3)$$

where the kernel function $S^{(2)}(\rho', \sigma', x', y')$ can be described, as follows:

$$S^{(2)}(\rho', \sigma', x', y') = \frac{1}{\sqrt{A'(\rho' - x')^2 + B'(\rho' - x')(\sigma' - y') + C'(\sigma' - y')^2}} + \frac{1}{\sqrt{A'(\rho' - x')^2 - B'(\rho' - x')(\sigma' + y') + C'(\sigma' + y')^2}}$$

$$A' = 1 + \zeta'_x{}^2(Q)$$

$$B' = 2\zeta'_x{}^2(Q)\zeta'_y{}^2(Q)$$

$$C' = 1 + \zeta'_y{}^2(Q) \quad (4)$$

Now, the integral $I'(Q)$ contains the singularity and is computed exactly in terms of logarithms:

$$\iint \frac{dsdt}{[As^2 + Bst + Ct^2]} = \frac{t}{A^{\frac{1}{2}}} \ln(2As + Bt + 2[A(As^2 + Bst + Ct^2)]^{\frac{1}{2}}) + \frac{s}{C^{\frac{1}{2}}} \ln(2Ct + Bs + 2[C(As^2 + Bst + Ct^2)]^{\frac{1}{2}}) + h_1(s) + h_1(t) \quad (5)$$

Moreover, the free surface conditions can be simplified by the symbol $\phi'(x', y')$. Then the kinematic and dynamic boundary conditions of the free surface are combined to be

$$\frac{(1 + \zeta'_x{}^2)\phi'_y{}^2 + (1 + \zeta'_y{}^2)\phi'_x{}^2 - 2\zeta'_x\zeta'_y\phi'_x\phi'_y}{2(1 + \zeta'_x{}^2 + \zeta'_y{}^2)} + \frac{\zeta'}{F'^2} = \frac{1}{2} \quad (6)$$

To solve the above nonlinear problem numerically, the $N \times M$ mesh is established on the free surface (N and M represent the number of longitude and latitude lines of the mesh, respectively). The x -coordinates and y -coordinates of nodes are x'_1, x'_2, \dots, x'_N and y'_1, y'_2, \dots, y'_M with intervals $\Delta x'$ and $\Delta y'$ in the x - and y - directions. The free surface elevation $\zeta'(x', y')$ and the velocity potential $\phi'(x', y')$ are represented by discrete point values $\zeta'_{k,l}$ and $\phi'_{k,l}$ at the nodes $(x'_k, y'_l), k = 1, \dots, N, l = 1, \dots, M$. And this paper chooses the x - derivatives of the functions ϕ' and ζ' as the basis for the solutions, together with

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the values of ϕ' and ζ' at the upstream boundary of the truncated domain, resulting in the vector of $2(N + 1)M$ unknowns \mathbf{u} to be

$$\mathbf{u} = [\phi'_{1,1}, (\phi'_x)_{1,1}, \dots, (\phi'_x)_{N,1}, \dots, \phi'_{1,M}, (\phi'_x)_{1,M}, \dots, (\phi'_x)_{N,M}, \zeta'_{1,1}, (\zeta'_x)_{1,1}, \dots, (\zeta'_x)_{N,1}, \dots, \zeta'_{1,M}, (\zeta'_x)_{1,M}, \dots, (\zeta'_x)_{N,M}]^T. \quad (7)$$

Therefore, The boundary integral equation can be discretized to the $(N - 1)M$ nonlinear equations, and an additional $(N - 1)M$ nonlinear equations are given by evaluating the free surface condition at the half mesh points. Moreover, $4M$ equations are provided by applying the radiation condition as follows. There are total $2(N + 1)M$ equations to be solved for the $2(N + 1)M$ unknowns in the nonlinear ship wave problem.

$$x'_1((\phi'_x)_{1,l} - 1) + \gamma(\phi'_{1,l} - x'_1) = 0 \quad (8)$$

$$x'_1((\phi'_{xx})_{1,l} - 1) + \gamma((\phi'_x)'_{1,l} - 1) = 0 \quad (9)$$

$$x'_1(\zeta'_x)_{1,l} + \gamma\zeta'_{1,l} = 0 \quad (10)$$

$$x'_1(\zeta'_{xx})_{1,l} + \gamma(\zeta'_x)_{1,l} = 0 \quad (11)$$

where γ is the decay coefficient.

Furthermore, more details about the governing equations, the boundary integral method and numerical discretization are provided by Sun[40].

3. Banded Preconditioner JFNK Algorithm

3.1. Jacobian-free Newton-Krylov method

JFNK method combines inexact Newton iteration method with Krylov subspace method. Its core content is the Generalized Minimum Residual (GMRES) algorithm, according to the matrix free idea, uses the finite difference form to approximate the product of coefficient matrix and vector, avoiding the Jacobian matrix calculation and storage alone.

After numerical discretization, a nonlinear system of equations could be obtained, as follows:

$$F(\mathbf{u}) = 0 \quad (12)$$

where \mathbf{u} is the vector of unknowns of the length $2(N + 1)M$.

JFNK method mainly has two processes, namely external and internal iterations. The external iteration is the inexact Newton iteration method, and the damping parameter λ_k is used to ensure that the nonlinear residual decreases significantly in each iteration for $t = 0, 1, 2, \dots$, as follows:

$$\mathbf{u}_{t+1} = \lambda_k \delta \mathbf{u}_t + \mathbf{u}_t, \lambda_k \in (0, 1] \quad (13)$$

Its internal iteration is GMRES algorithm [47], which efficiently solves the correction in inexact Newton iteration, that is, computes large-scale linear equations as follows:

$$\mathbf{J}(\mathbf{u}_t) \delta \mathbf{u}_t = -F(\mathbf{u}_t) \quad (14)$$

where $\mathbf{J}(\mathbf{u}_t) = \partial F(\mathbf{u}_t) / \partial \mathbf{u}_t$ is the Jacobian matrix [48].

The GMRES method is one of the Krylov subspace methods which are attractive as linear solvers in the context of nonlinear Newton iteration. Since each iteration does not require the exact value of $\delta \mathbf{u}_t$ in solving nonlinear equations, the GMRES algorithm fits this need well and thus can increase total computation speed.

Firstly, the approximate solution of δu_t is found by projecting obliquely onto the Krylov subspace

$$K_m(\mathbf{J}_t \mathbf{P}^{-1}, F_t) = \text{span}\{F_t, \mathbf{J}_t \mathbf{P}^{-1} F_t, \dots, (\mathbf{J}_t \mathbf{P}^{-1})^{m-1} F_t\} \quad (15)$$

where m is the value of the subspace dimension and the accuracy of the solution increases with the subspace dimension, $\mathbf{J}_t = \mathbf{J}(u_t)$, $F_t = F(u_t)$. The matrix $\mathbf{P} \approx \mathbf{J}_t$ is the preconditioner matrix, whose purpose is to construct an approximation to the Jacobian \mathbf{J}_t which is cheap to form and to factorize. The calculation speed of the GMRES method can be significantly improved with a preconditioner matrix, because the spectrum of the preconditioned Jacobian $\mathbf{J}_t \mathbf{P}^{-1}$ exhibits a clustering of eigenvalues [49].

An initial linear residual r_0 is defined, given an initial guess u_0 , for the Newton correction,

$$r_0 = -F(u_0) - \mathbf{J}_0 \mathbf{P}^{-1} \delta u_0 \quad (16)$$

Subsequently, the GMRES iteration minimizes $\|r_t\|$ to a suitable value. These Jacobian-vector products can be approximated by applying first-order difference quotients:

$$\mathbf{J}_t \mathbf{P}^{-1} v \approx \frac{F(u_t + h \mathbf{P}^{-1} v) - F(u_t)}{h} \quad (17)$$

where v represents an arbitrary vector used in building the Krylov subspace [50], and the h is a small perturbation

$$h = \frac{\sqrt{(1 + \|u_t\|) h_{mach}}}{\|v\|} \quad (18)$$

Finally, as for the nonlinear ship wave problem, the initial guess u_0 can be defined as below. The nonlinear equations are solved according to the calculation flow of banded preconditioner JFNK method, as shown in Figure 2. Note that v in the figure is a unit orthogonal vector in the orthonormal basis of Krylov subspace.

$$\phi'_{1,l} = x'_0, (\phi'_x)_{k,l} = 1, \zeta'_{1,l} = 0, (\zeta'_x)_{k,l} = 0, \quad k = 1, \dots, N \text{ and } l = 1, \dots, M. \quad (19)$$

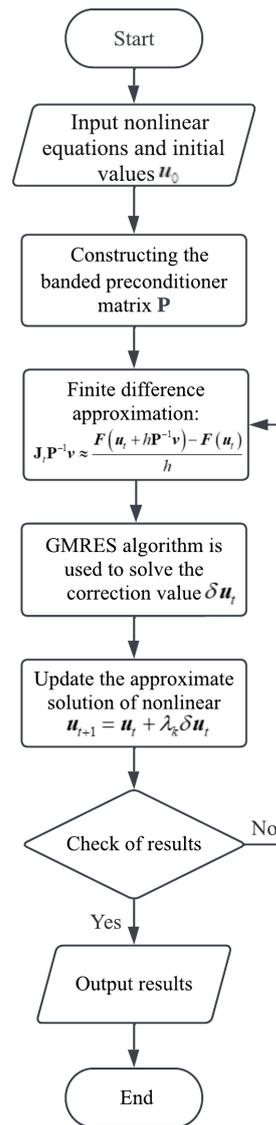


Figure 2. Calculation flow chart of the banded preconditioner JFNK method

3.2. Banded preconditioner method

Iterative methods, e.g. GMRES method etc., are currently most popular choices for solving large sparse linear systems of equations. However, this process of preconditioning is essential to most successful application of iterative methods, since the convergence of a matrix iteration depends on the properties of the matrix, e.g. the eigenvalue, etc., [51]. Generally, the methods for choosing the appropriate preconditioner are different for the specific problems. In this section, a banded preconditioner method for solving the nonlinear ship wave problem is proposed.

3.2.1. Building preconditioner matrix

For a good preconditioner P , it should be cheap to form and to factorize. Meanwhile, the preconditioned Jacobian $J_t P^{-1}$ should be easier to solve, which means the eigenvalues are more concentrated. In general, it is feasible to consider a matrix constructed from the same problem under simplified physics [49]. This paper applies the numerical scheme to the linearized governing equations which apply formally in the limit $\epsilon' \rightarrow 0$.

The equations of the linear problem of computing the Havelock potential for flow past a submerged point source can be described, as follows [25,52–54]:

$$\zeta'_x = \phi'_z \quad \text{on } z' = \zeta'(x', y') \quad (20)$$

$$\phi'_x - 1 + \frac{\zeta'}{F'^2} = 0 \quad \text{on } z' = \zeta'(x', y') \quad (21)$$

According to the linear free surface boundary condition, the boundary integral equation can be described, as follows:

$$2\pi(\phi'(Q) - x') = -\frac{\epsilon'}{(x'^2 + y'^2 + 1)^{\frac{1}{2}}} + \int_0^\infty \int_{-\infty}^\infty \phi'_\rho(P) K^{(3)}(\rho', \sigma', x', y') d\sigma' d\rho' \quad (22)$$

where

$$K^{(3)}(\rho', \sigma', x', y') = \frac{1}{[(\rho' - x'^2)^2 + (\sigma' + y')^2]^{\frac{1}{2}}} + \frac{1}{[(\rho' - x'^2)^2 + (\sigma' - y')^2]^{\frac{1}{2}}}$$

After numerical discretization, the linear system can be described, as follows:

$$\mathbf{F1}_{k,l} = \phi_{k,l}(Q) + \frac{\zeta'_{k,l}(Q)}{F'^2} - 1 \quad (23)$$

$$\mathbf{F2}_{k,l} = 2\pi(\phi'_{k,l}(Q) - x'_k) + \frac{\epsilon'}{[x'^2_k(Q) - y'^2_l(Q) + 1]^{\frac{1}{2}}} - \sum_{i=1}^N \sum_{j=1}^M w(i,j) [(\zeta'_\rho)_{i,j} - (\zeta'_x)_{i,j}] K^{(3)}_{i,j,k,l} - (\zeta'_x)_{i,j} I \quad (24)$$

where $w(i, j)$ is the weighting function for numerical integration, for $k = 1 \dots (N - 1)$, $l = 1 \dots M$. Then the linear Jacobian can be calculated directly, by differentiating the linear system with respect to $\phi'_{1,m'}$, $(\phi'_x)_{n,m'}$, $\zeta'_{1,m}$ and $(\zeta'_x)_{n,m}$. Therefore, the preconditioner matrix \mathbf{P} can be formed cheaply, and the eigenvalues of $\mathbf{J}_t \mathbf{P}^{-1}$ obviously cluster as shown in Figure 3. 148
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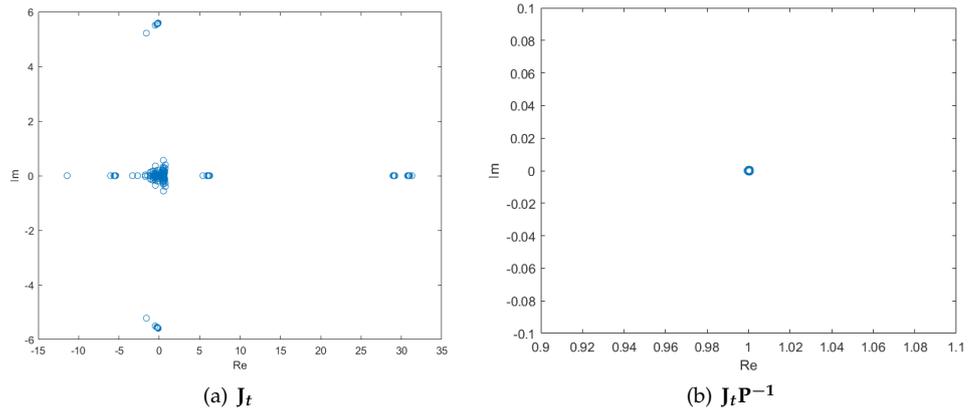


Figure 3. A plot of the spectrum for \mathbf{J}_t and $\mathbf{J}_t \mathbf{P}^{-1}$ on a 31×11 mesh.

3.2.2. Preconditioner factorisation and storage 153

The JFNK method requires the result of the product of the inverse preconditioner matrix and vector, $\mathbf{P}^{-1}v$. In general, the operation of inverting a matrix should be converted to solving a system of linear equations, $\mathbf{P}r = v$. Find the solution r , the result of $\mathbf{P}^{-1}v$ will

be got. In order to calculate this linear system conveniently, the preconditioner can be divided up into four submatrices and factorized using the block decomposition,

$$\mathbf{P} = \begin{bmatrix} \mathbf{A} & \mathbf{B} \\ \mathbf{C} & \mathbf{D} \end{bmatrix} = \begin{bmatrix} \mathbf{I} & \mathbf{0} \\ \mathbf{CA}^{-1} & \mathbf{I} \end{bmatrix} \begin{bmatrix} \mathbf{A} & \mathbf{0} \\ \mathbf{0} & \mathbf{D} - \mathbf{CA}^{-1}\mathbf{B} \end{bmatrix} \begin{bmatrix} \mathbf{I} & \mathbf{A}^{-1}\mathbf{B} \\ \mathbf{0} & \mathbf{I} \end{bmatrix} \quad (25)$$

where \mathbf{I} is the unit matrix, \mathbf{A} , \mathbf{B} , \mathbf{C} and \mathbf{D} are the four submatrices which are constructed on the base of Jacobian \mathbf{J}_t . 154
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Accordingly, the vector v can be divided into upper and lower parts $[v_1 \ v_2]^T$, and then the solution r can be got after three cheap steps, as follows:

$$\begin{bmatrix} o_1 \\ o_2 \end{bmatrix} = \begin{bmatrix} v_1 \\ v_2 - \mathbf{CA}^{-1}v_1 \end{bmatrix} \quad (26)$$

$$\begin{bmatrix} s_1 \\ s_2 \end{bmatrix} = \begin{bmatrix} \mathbf{A}^{-1}o_1 \\ (\mathbf{D} - \mathbf{CA}^{-1}\mathbf{B})^{-1}o_2 \end{bmatrix} \quad (27)$$

$$\begin{bmatrix} r_1 \\ r_2 \end{bmatrix} = \begin{bmatrix} s_1 - \mathbf{A}^{-1}\mathbf{B}s_2 \\ s_2 \end{bmatrix} \quad (28)$$

The calculation of $\mathbf{P}^{-1}v$ in the Eq. (17) can be facilitated according to the progressive order from Eqs. (26) - (28). The reasons are as follows: the submatrix \mathbf{A} is tridiagonal, allowing for easy storage and fast factorization; the submatrices \mathbf{B} and \mathbf{C} are only used in matrix vector multiplication operations and thus can be implemented as functions that perform these operations rather than stored as matrices. 156
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3.2.3. The banded preconditioner 161

After the factorisation operation, the calculation and storage of preconditioner matrix \mathbf{P} are optimized. However, the size of submatrix \mathbf{D} is $(N+1)M \times (N+1)M$, it will increase dramatically as the size of mesh increases. Consequently, there will be two problems when the preconditioner matrix size is large. One is a memory problem, the running memory of this computer cannot accommodate this preconditioner matrix; the other is an efficiency problem, inverting the preconditioner matrix will take much time. 162
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By observing the preconditioner matrix, it can be found that the values decay with distance from the main block diagonal. This observation suggests using a banded approximation to the matrix for our preconditioner, as shown in Figure 4. Moreover, batch construction avoids the problem of insufficient running memory due to the large size of the submatrix \mathbf{D} . The compressed sparse row (CSR) data format is used to save this matrix. Hence, a lot of memory can be saved. 168
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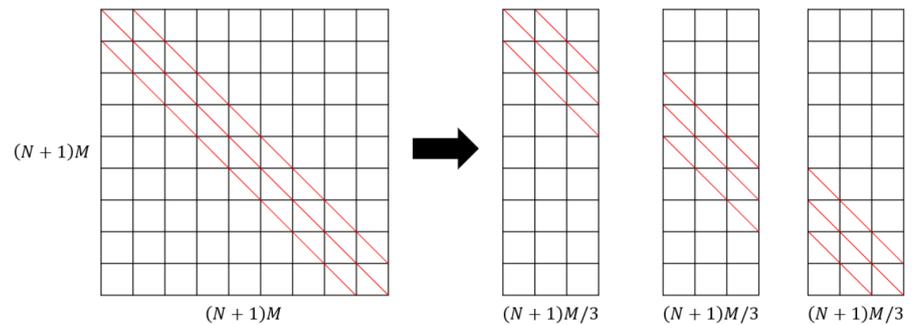


Figure 4. Construction of the banded preconditioner

The feasibility of the banded preconditioner matrix method is verified, as shown in Figure 5. The tightness of clustering can be further improved by increasing the bandwidth. When the $band = 21$, the eigenvalues of $\mathbf{J}_t \mathbf{P}^{-1}$ have been clustered, satisfying the requirement of the GMRES method.

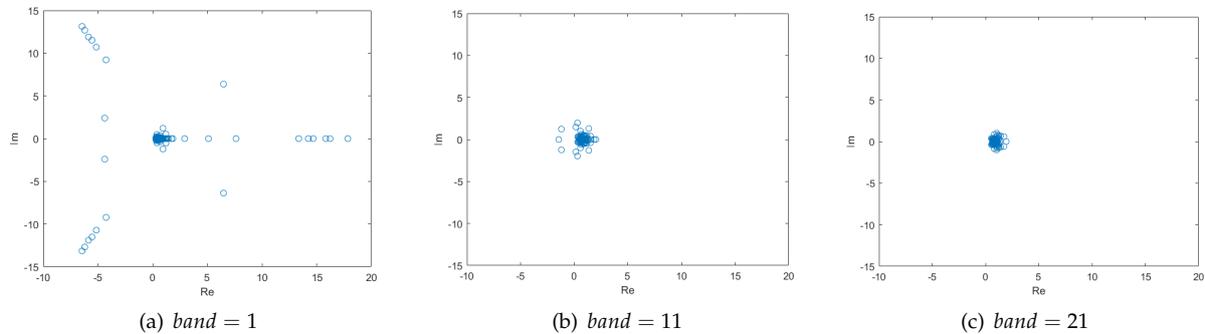


Figure 5. A plot of the spectrum for \mathbf{J}_t and $\mathbf{J}_t \mathbf{P}^{-1}$ on a 31×11 mesh for: $band = 1$, $band = 11$, $band = 21$.

For certain bandwidth values, the computing speed of GMRES will not be significantly improved by increasing the bandwidth further. However, the time required for inverse operation will increase in these cases as the banded preconditioner matrix size increases. The bandwidth regulates the runtime of inverting banded preconditioner matrix and the number of the inner iterations of the GMRES method. The runtime of inverting banded preconditioner matrix increases with the bandwidth, while the inner iterations decrease with the bandwidth. Therefore, the total runtime will decrease first and then increase with the bandwidth, as shown in Figure 6. The case is $F' = 0.7$ and $\epsilon' = 0.4$, computed on a 121×41 mesh, when b' (bandwidth $band = b' \times (N + 1)$) is less than 14, an ill-conditioned coefficient matrix is formed, the accuracy of the solution is low. The runtime decreases with b' ranges from 14 to 16, then the runtime increases monotonically with b' ranges from 16 to 20. For the case of 121×41 mesh, the shortest running time is 5.6s with the optimal bandwidth $band = 16 \times (N + 1)$. Therefore, provided that the appropriate bandwidth is selected, not only can save memory, but also can improve the computational efficiency.

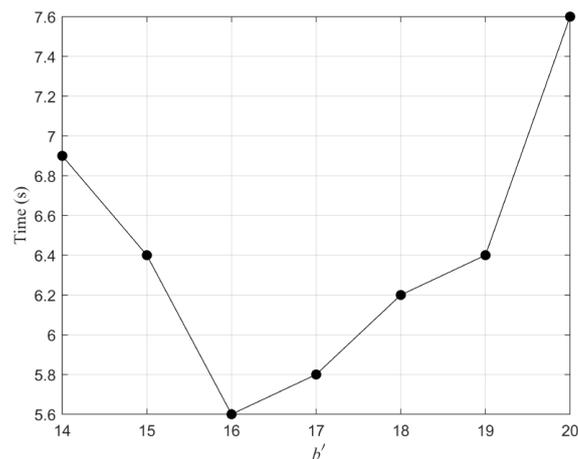


Figure 6. The plot of runtime against the bandwidth, computed on a 121×41 mesh with $\Delta x' = 0.3$, $\Delta y' = 0.3$, $band = b' \times (N + 1)$.

4. GPU Parallel Computing Framework

Although the banded preconditioner JFNK algorithm can improve the computational efficiency of nonlinear ship wave problem, the running time of the program will increase significantly with the increase of the mesh size, which is very unfavorable to the further

study of nonlinear ship wave. The reason is that the CPU is not good at handling such large-scale nonlinear equations. Compared with CPU, GPU possesses more arithmetic logic units in the same chip area [55]. This hardware framework makes GPU own plenty of threads naturally to process large amounts of data simultaneously. The computational efficiency of nonlinear ship wave can be greatly improved by utilizing the GPU acceleration technique .

4.1. Parallel computing framework design

In this paper, Compute Unified Device Architecture (CUDA) language is used to develop the numerical scheme for computing ship wave patterns. CUDA is a parallel computing platform and programming model created by NVIDIA and implemented by GPU [56]. CUDA toolkit includes abundant GPU accelerated libraries, optimization tools and a runtime library, which can be compiled in C language, C++ language and Fortran language. In addition, CUDA source program can be executed on multiple GPUs. By applying the hybrid programming model, the parallel computing process consists of kernel function on device and serial code on host CPU. Therefore, a typical CUDA program is consisted of two parts: a host part runs on CPU and a device part runs on GPU. The host code is responsible for the logic work, environment configuration, instructions to set up parallelism, and data communication between the host and the device. The main responsibility of device code is to process tasks in parallel, and the dimensions of the grid and thread are specified by configuration parameters in the kernel function. Figure 7 shows the CUDA execution mode and thread hierarchy.

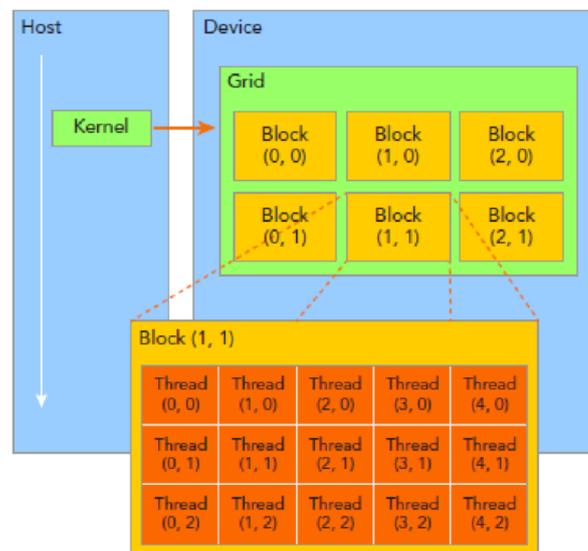


Figure 7. Illustration of CUDA execution mode and thread organization hierarchy [56]

As for the solver of ship wave pattern, as described above, there are four main parts: building preconditioner matrix, creating nonlinear system, inverting preconditioner matrix and solving linear equations by GMRES algorithm. Simulation results of the CPU solver proposed by Sun[40] show that the parts of creating nonlinear system and inverting preconditioner matrix take up most of the time, as shown in Figure 8. This figure shows the computation time distribution of the CPU solver on a 151×51 mesh case. The total runtime is 185.8 seconds, in which the runtime of inverting preconditioner matrix and creating nonlinear system is 95.8 and 80.4 seconds respectively. Each of them takes up nearly half the total runtime. Therefore, calculations on these two parts parallelly are vital for improving computational efficiency. And the part of building preconditioner matrix and solving linear equations will also be executed in GPU to further shorten the program running time.

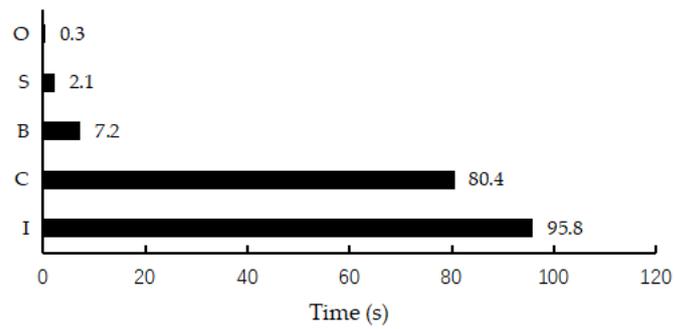


Figure 8. The computation time distribution of ship wave solver. The alphabet I represents the part of inverting preconditioner matrix, the alphabet C represents the part of creating nonlinear system, the alphabet B represents the part of building preconditioner matrix, the alphabet S represents the part of solving linear equations by GMRES algorithm and the alphabet O represents the part of other code in the solver.

Based on the above analysis, the GPU solver of Kelvin ship waves adopts a hybrid programming model. The entire parallel computing procedure is shown as follows :

Step 1: Input calculation parameters including the initial guess, the data is transferred from CPU to GPU;

Step 2: According to the calculation parameters, the nonlinear equations are created in the GPU device;

Step 3: The banded preconditioner method is applied to build the banded preconditioner matrix in GPU;

Step 4: QR decomposition algorithm is used to invert the preconditioner matrix, and saving the decomposition results outside the loop body to avoid repeated QR decomposition of preprocessing;

Step 5: The result of $P^{-1}v$ is calculated directly using the QR decomposition results, by combining the result of $P^{-1}v$ with the approximate solution u of the nonlinear equations, the finite difference approximation is carried out to obtain the linear equations;

Step 6: The GMRES algorithm is used to calculate the linear equations, obtain the correction values and update the approximate solutions u ;

Step 7: Check of the approximate solutions of the nonlinear equations: If the accuracy requirement is not met, back to Step 5; if the accuracy requirement is met, the result is transferred from the GPU to the CPU.

The corresponding calculation flow chart is shown in [Figure 9](#), which shows the calculation procedure more clearly.

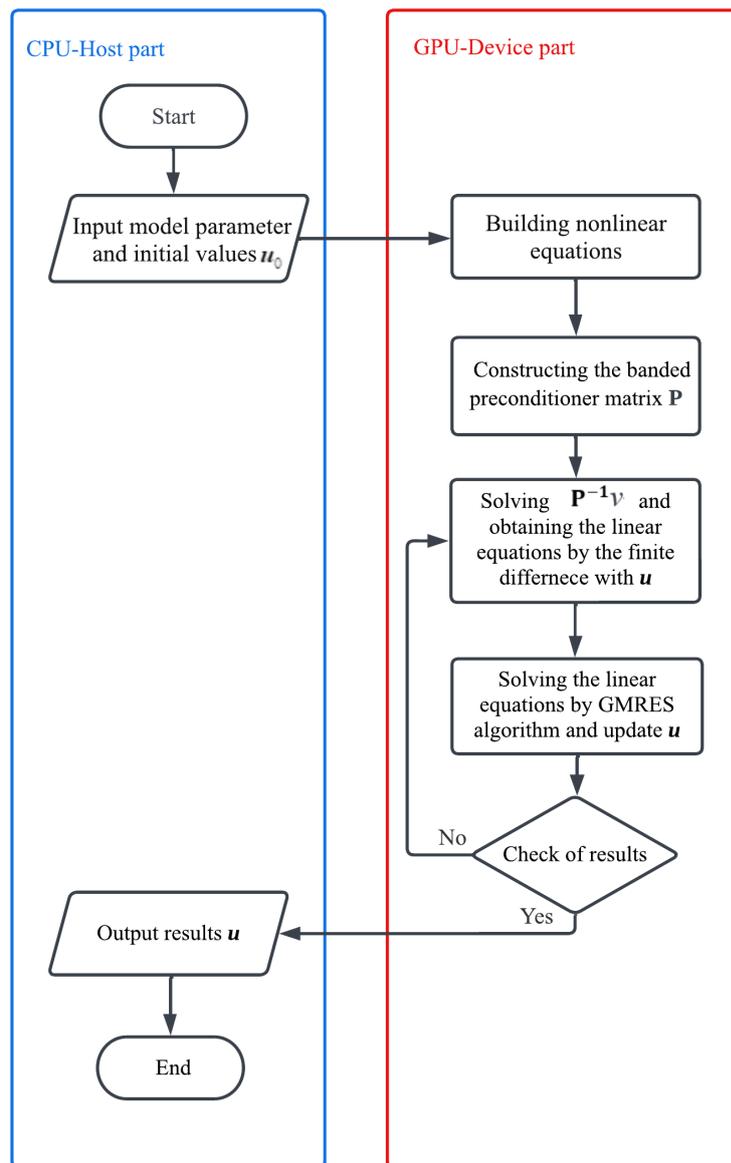


Figure 9. The computational flow chart of GPU implementation.

4.2. GPU solver implementation

4.2.1. Creating nonlinear system

The programming for creating nonlinear system on GPU by using CUDA language is briefly shown in Table 1. On the whole, the dimension of the GPU grid is the equivalent of the size of mesh, which means that one block can complete the relevant equations of one node in mesh. One block has 1024 threads, these plenty of threads can calculate Eq. (1), Eq. (6) and Eqs. (8)-(11) simultaneously.

In the device part, there are two device functions which are called by the kernel function multiple times. According to Eq. (5), these two device functions are formed to solve the singularity in the second integral of the boundary integral equation. Eq. (5) is distributed into 16 special threads for fast computation. Other threads with the same CUDA code are used to complete the calculation of the remaining parts of the boundary integral equation. After threads have finished computing, all thread contributions are summed up, and $(N - 1)M$ nonlinear equations are built. Then arbitrarily choose 5 blocks to calculate the free surface condition and the radiation condition, $(N + 3)M$ nonlinear

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equations can be obtained. Therefore, $2(N + 1)M$ nonlinear equations are formed by using the $1024 \times (N - 1)M$ threads on GPU.

In the host part, the environment variables are configured firstly. Then the data is transferred from CPU to GPU and the parallel instruction is sent to GPU. Finally, CPU gets the computation results from GPU.

Table 1. The programming on GPU.

Device Part	
<i>Device function 1</i>	
1	<code>__device__ double Integral(double s,double t,double A,double B,double C)</code>
2	<code>{</code>
3	<code>double val= t/sqrt(A)*log(2*A*s + B*t+ 2*sqrt(A*(A*s*s+ B*s*t +C*t*t)));</code>
4	<code>return val;</code>
5	<code>}</code>
<i>Device function 2</i>	
1	<code>__device__ double Integrall(double s,double t,double A,double B,double C)</code>
2	<code>{</code>
3	<code>double val = s/sqrt(C)*log(2*C*t + B*s+ 2*sqrt(C*(A*s*s+ B*s*t +C*t*t)));</code>
4	<code>return val;</code>
5	<code>}</code>
<i>Kernel function</i>	
1	<code>__global__ void nonlinear(const long int N, const long int M, ...)</code>
2	<code>{</code>
3	<code>__shared__ double A, B, C, ... ;</code>
4	<code>__shared__ long int K, l, blockpos, ... ;</code>
5	<code>long int i, j, threadPos, ... ;</code>
6	<code>threadPos = threadIdx.x; k = blockIdx.x; l = blockIdx.y ;</code>
7	<code>// Calculate necessary values</code>
8	<code>while(threadPos<(M*N)) { ... }</code>
9	<code>// Calculation of the 16 parts to the closed integral</code>
10	<code>if(threadIdx.x==blockDim.x-1) { ... }</code>
11	<code>...</code>
12	<code>if(threadIdx.x==blockDim.x-16) { ... }</code>
13	<code>// Sum up all thread contributions</code>
14	<code>for(i=blockDim/2;i>0;i=i/2){ ... }</code>
15	<code>//Split the free surface condition and radiation conditions between 5 blocks</code>
16	<code>if(k==0&&l==0) { ... } ... if(k==0&&l==5) { ... }</code>
17	<code>}</code>
Host Part	
1	<code>int main</code>
2	<code>{</code>
3	<code>// Initialize data on CPU</code>
4	<code>InitialData(double* cpuData, double* gpuData);</code>
5	<code>long int i, j, threadPos, ... ;</code>
6	<code>// Copy data from CPU to GPU</code>
7	<code>cudaMemcpy(cpuData, gpuData, Datasize, cudaMemcpyHostToDevice);</code>
8	<code>// Set dimensions of the grid and thread</code>
9	<code>dim3 block(1024, 1); dim3 grid(M, N - 1);</code>
10	<code>// Start Kernel function</code>
11	<code>Nonlinear<< grid, block<<(gpuData);</code>
12	<code>// Copy data from GPU to CPU</code>
13	<code>cudaMemcpy(gpuData, cpuData, Datasize, cudaMemcpyDeviceToHost);</code>
14	<code>return 0;</code>
15	<code>}</code>

4.2.2. Building preconditioner matrix

The building preconditioner matrix is decomposed to several tasks that can be operated in parallel by corresponding kernel functions in GPU blocks. The parallel idea and program structure are roughly similar to the part of creating nonlinear equations.

In the device part, in order to avoid data storage conflicts in GPU, three kernel functions are used to construct the preconditioner matrix in turn. As mentioned above, the preconditioner matrix size is $2(N + 1)M \times 2(N + 1)M$, and four submatrices are formed by block decomposition method and banded preconditioner method: Submatrix **A** is a tridiagonal matrix of size $3 \times (N + 1)M$; the submatrix **B** and **C** only differ between coefficients, and the base matrix **B**₀ can be constructed to represent them respectively with a size of $(N + 1) \times (N + 1)$, **B** = $1/F'^2 \cdot \mathbf{B}_0$ and **C** = $2\pi \cdot \mathbf{B}_0$; the submatrix **D** is a sparse matrix with a size of $band \times (N + 1)M/3$. Firstly, $M \times (N + 1)$ thread blocks are called in the GPU to fulfill the parallel construction of the four submatrices by the kernel function precondition(), and the two device functions mentioned above are also used to eliminate the singularity of the linear boundary integral equation. Then the kernel function matrix() is written to call $N + 1$ thread blocks for parallel computation of $\mathbf{C}\mathbf{A}^{-1}\mathbf{B}$, which involves solving multiple right-handed linear systems and matrix multiplication. Finally, the subtraction operation between matrices is completed by kernel function Schur(), and $M \times (N + 1)$ thread blocks are called to perform parallel operation of $\mathbf{D} - \mathbf{C}\mathbf{A}^{-1}\mathbf{B}$. The programming for building preconditioner matrix on GPU by using CUDA language is briefly shown in Table 2.

In the host part, the variables are first defined according to calculation parameters, then data is transferred from the CPU to the GPU, then the dimension of the thread blocks and thread grid is specified, and finally the kernel functions precondition(), matrix(), and Schur() are successively released. This part of the host side code is similar to the establishment of nonlinear equations and will not be repeated here.

Table 2. The programming on GPU.

Device Part	
<i>Kernel functions</i>	
1	<code>__global__ void precondition(const long int N, const long int M, ...)</code>
2	<code>{</code>
3	<code> // Calculate submatrix A</code>
4	<code> while(threadPos<N+3) { ... };</code>
5	<code> if(blockIdx.x==0) { ... };</code>
6	<code> // Calculate basis matrix B₀</code>
7	<code> if(blockIdx.x==0) { while(threadPos<N+1) { ... } };</code>
8	<code> // Calculate submatrix D</code>
9	<code> while(threadPos<N*M) { ... };</code>
10	<code> // Sum up all thread contributions</code>
11	<code> for(i=blockDim/2;i>0;i=i/2){ ... }</code>
12	<code>}</code>
13	<code>__global__ void matrix(const long int N, const long int M, ...)</code>
14	<code>{</code>
15	<code> // Calculate CA⁻¹B</code>
16	<code> double Bcoef = 1/(F' × F');</code>
17	<code> double Ccoef = 2*3.1415;</code>
18	<code> if(blockIdx.x==0) { while(threadPos<N+1) { ... } };</code>
19	<code>}</code>
20	<code>__global__ void Schur(const long int N, const long int M, double* _b, double* D)</code>
21	<code>{</code>
22	<code> // Calculate D – CA⁻¹B</code>
23	<code> int l=blockIdx.x;</code>
24	<code> int k=blockIdx.y;</code>
25	<code> int pos=1*(N+1)+k;</code>
26	<code> while(threadPos<N+1)</code>
27	<code> {</code>
27	<code> D[(1*(N+1)+threadPos)*M*(N+1)+pos]=D[(1*(N+1)+</code>
28	<code> threadPos)*M*(N+1)+pos]-_b[threadPos*(N+1)+k];</code>
29	<code> threadPos+=blockDim.x;</code>
30	<code> }</code>
31	<code>}</code>

4.2.3. Inverting preconditioner matrix

Comparing the Math Kernel Library which is famous for the computation of sparse linear algebra, the cuSolverSP library is generally faster for solving sparse linear systems [57]. In this paper, the cuSolverSP library is adopted to invert preconditioner matrix. The present sparse linear system is special, the right-hand term of the system v changes continuously in the iteration whereas the left-hand term does not. The characteristic of the sparse linear system suggests using QR factorization to calculate $\mathbf{P}^{-1}v$ [51]. By QR factorization, the sparse matrix is decomposed into an orthogonal matrix and an upper triangular matrix, which are saved in GPU memory and are directly used to solve linear equations in each iteration. Finally, the preconditioner-vector products $\mathbf{P}^{-1}v$ can be obtained.

Step1: Using CSR data format to save preconditioner matrix with an appropriate bandwidth;

Step2: In the analysis stage, `cusolverSpXcsrqrAnalysis()` function is used to analyze the sparsity of orthogonal matrix and upper triangular matrix in QR decomposition. This process may consume a large amount of memory. If the memory is insufficient to complete the analysis, the program will stop running and return the corresponding error message;

Step3: In the preparation stage, `cusolverSpXcsrqrAnalysis()` function is used to select the appropriate computing space to prepare for QR decomposition. Here, two memory

Table 3. The list of CUDA functions for QR factorization

No.	Function name	Goal
1	cusolverSpXcsrqrAnalysisHost();	Analyze structure
2	cusolverSpDcsrqrBufferInfoHost();	Set up workspace
3	cusolverSpDcsrqrSetupHost();	QR factorization
4	cusolverSpDcsrqrFactorHost();	QR factorization
5	cusolverSpDcsrqrZeroPivotHost();	Check singular
6	cusolverSpDcsrqrSolveHost();	Solve system

blocks are prepared in the GPU, one to store the orthogonal matrix and the upper triangular matrix, and the other to perform QR decomposition; 314 315

Step4: The `cusolverSpDcsrqrSetup()` function is called to allocate storage space for the orthogonal and upper triangular matrices based on the results of the preparation stage. Then, `cusolverSpDcsrqrFactor()` function is used to complete the QR decomposition of coefficient matrix outside the cycle; 316 317 318 319

Step5: Using `cusolverSpDcsrqrZeroPivot()` function checks the singularity of the decomposition results, if the nearly singular the program terminates operation and error is given, return to step 1 to choose the bandwidth again; 320 321 322

Step6: In the loop body, the `cusolverSpDcsrqrSolve()` function is repeatedly called, and the solution of linear equations can be obtained directly by using the decomposition results stored in GPU; 323 324 325

The main CUDA functions are shown in [Table 3](#). 326

4.2.4. Solving linear equations by GMRES algorithm 327

In the process of solving linear equations, because the matrix free idea is adopted to avoid the storage of coefficient matrix, there is no product operation of coefficient matrix and vector in GMRES algorithm, so the operations that can be parallel in this part are operations between vectors. Therefore, this paper mainly uses cuBLAS library to complete the CUDA programming of GMRES algorithm to solve linear equations. 328 329 330 331 332

The `cuBlasDdot()` function is used to realize the inner product of vectors in the GMRES algorithm; the vector subtraction is calculated using `cublasDaxpy()` function; `cublasDnrm2()` function is used to calculate the Euclidean norm of the vector; `cublasDscal()` function is used to divide vector and scalar. After obtaining the orthonormal basis of Krylov subspace and the upper Hessenberg matrix, `cublasDrotg()` function is used to perform Givens rotation transformation on the upper Hessenberg matrix in GPU device to obtain the upper triangular matrix. Then the solution of linear least squares problem in GMRES algorithm is obtained, and `cublasDspmv()` function is used to achieve orthonormal basis and vector multiplication to get the solution of linear equations. 333 334 335 336 337 338 339 340 341

5. Numerical Simulations and Discussion 342

In this section, numerical simulation of ship wave in multiple cases are carried out using the CPU and GPU solvers, and the simulation results are discussed. The effectiveness of the developed banded preconditioner JFNK method is first verified. Then, comparisons between the proposed GPU solver and the CPU solver on accuracy and efficiency are performed. Finally, the capability of GPU solver is verified by comparing simulation results with real ship wakes. 343 344 345 346 347 348

Both CPU and GPU solvers are executed on a high-performance computing cluster, the CPU Clock Speed is 1.90 GHz, the GPU card is NVIDIA Tesla A100, which includes 6912 CUDA cores and 40 GB graphics memory. The parameters of the computing environment are listed in [Table 4](#). 349 350 351 352

Table 4. The computing environment of high-performance computing cluster

	CPU	GPU
Card	Intel Xeon Bronze 3204	NVIDIA Tesla A100
Memory	64 GB	40 GB
Max Cores	6 per node	6912
Programming language	C++	CUDA, C++

Table 5. The running memory usage before and after applying the banded preconditioner method

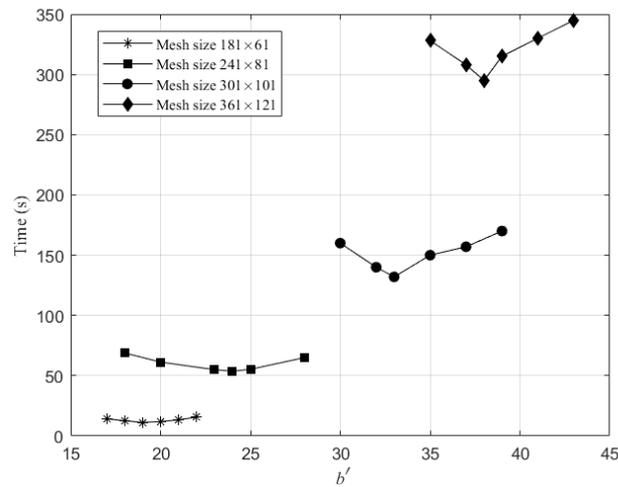
Mesh size	Before	b'	After	Reduction ratio
181×61	0.91GB	19	0.28GB	3.2
241×81	2.9GB	24	0.88GB	3.3
301×101	6.9GB	33	2.3GB	3.0
361×121	15GB	38	4.6GB	3.3

5.1. Verification of the banded preconditioner JFNK method

To reveal the effectiveness of the banded preconditioner method, numerical simulations on different mesh sizes, i.e., 181×61 , 241×81 , 301×101 and 361×121 with $\Delta x' = 0.3$, $\Delta y' = 0.3$ are carried out.

The overall runtimes against bandwidth on these four mesh sizes are illustrated in Figure 10. From this figure, it can be seen that an optimal value of bandwidth b' exists for a certain mesh size. Furthermore, the optimal value of b' increases with the mesh size and approximately equals $\frac{M}{3}$, in which M means the number of latitude lines of the mesh. Therefore, the optimal bandwidth can be set to $\frac{M}{3} \times (N + 1)$ to get an optimal efficiency, which is called the optimal bandwidth selection rule in this paper.

According to the optimal bandwidth selection rule, the running memories against the bandwidth are shown in Table 5. Correspondingly, the required running memory is drastically reduced by applying the banded preconditioner JFNK method. The mean reduction ratio is about 3.2, this means that the banded preconditioner JFNK method can save running memory by at least two-thirds.

**Figure 10.** Optimal values of bandwidth b' for different mesh sizes.

5.2. Verification of the GPU solver

5.2.1. Accuracy

To verify the accuracy of the GPU solver, numerical simulations are conducted on $F' = 0.7$ and $\epsilon' = 0.4$ with a 361×121 mesh and $\Delta x' = 0.3$, $\Delta y' = 0.3$. The simulated wave heights on the centerline are compared with that of the CPU solver proposed by Sun [40],

Table 6. The comparisons of runtime between CPU solver and GPU solvers on different mesh sizes, the CPU solver is proposed by Sun [40], one GPU solver is proposed in this paper, the other GPU solver is proposed by Pethiyagoda [42](results of Exp.).

Mesh size	CPU solver	Exp.	GPU solver	Accelerated-up ratio
121×41	8.96E+1 s	1.61E+1 s	5.60E+0 s	16.1
181×61	2.70E+2 s	1.22E+2 s	1.13E+1 s	23.9
241×81	8.70E+2 s	5.51E+2 s	5.37E+1 s	16.0
301×101	2.56E+3 s	1.78E+3 s	1.32E+2 s	19.3
361×121	6.08E+3 s	5.04E+3 s	2.95E+2 s	20.6

which is shown in Figure 11. From this figure, it can be seen that almost all points in the figure are traversed through the center by line, indicating that the calculation results of the GPU solver are very consistent with those of the CPU solver.

Furthermore, the Mean Square Error (MSE) is used to further explain the error between them, as follows:

$$MSE = \frac{1}{n} \sum_{i=1}^n (Truch_i - Value_i)^2 \quad (29)$$

where n is the amount of data, $Truch_i$ and $Value_i$ represent CPU results and GPU results respectively. According to Eqs.(29), the calculated MSE is 9.37E-8, indicating that the calculation error between the GPU and CPU solver is minimal. Since the CPU solver has been verified by Sun [40], the accuracy of the proposed GPU solver can also be acceptable.

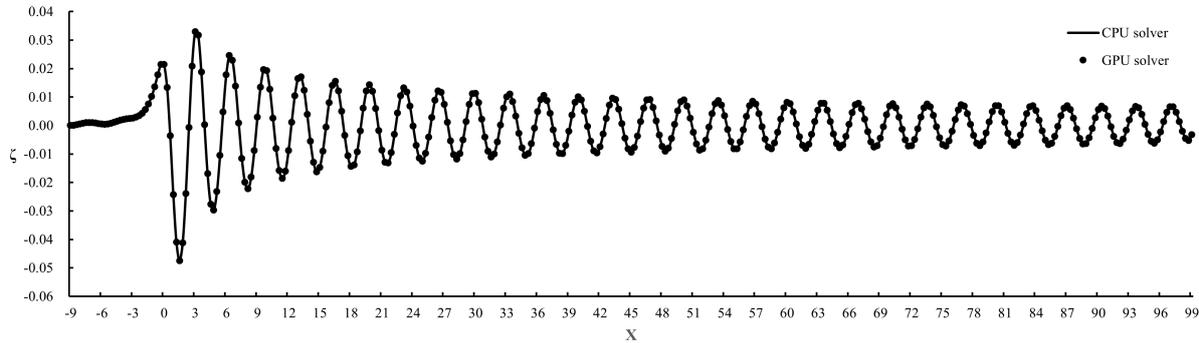


Figure 11. A comparison of the centerline profiles for the simulation results of CPU solver and GPU solver, computed on a 361×121 mesh with $\Delta x' = 0.3$, $\Delta y' = 0.3$, $F' = 0.7$ and $\epsilon' = 0.4$. The solid line represents the simulation result of the GPU solver, the solid circles represent the simulation result of the CPU solver.

5.2.2. Efficiency

To verify the efficiency of the GPU solver, numerical simulation are conducted on $F' = 0.7$ and $\epsilon' = 0.4$ with five mesh sizes, namely 121×41 , 181×61 , 241×81 , 301×101 , 361×121 and $\Delta x' = 0.3$, $\Delta y' = 0.3$. The overall runtimes of GPU solver are compared with that of the CPU solver proposed by Sun [40], as shown in Figure 12. From this figure, it can be seen clearly that the overall runtimes of the GPU solver are much shorter than that of the CPU solver. The clear accelerated-up ratios between the GPU solver and the CPU solver are shown in Table 6. From this table, it can be seen that the accelerated-up ratio on all cases are around 20.0. Therefore, the computation efficiency of the proposed GPU solver is much higher than the CPU solver.

The proposed GPU solver has also been compared with another GPU solver proposed by Pethiyagoda [42] on these cases. The comparison of computation time between them is shown in Table 6. Obviously, the efficiency of the GPU solver proposed in this paper is higher than the GPU solver proposed by Pethiyagoda [42], and the advantage is more

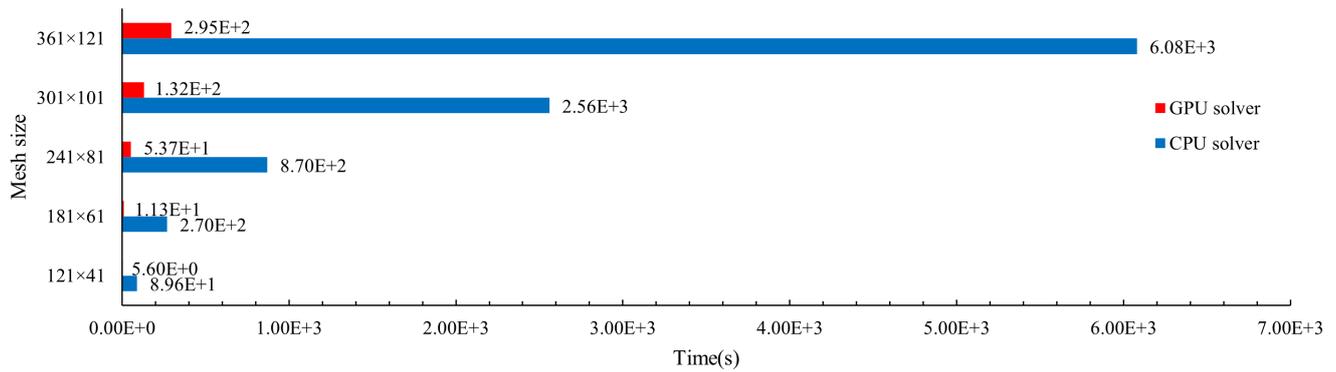


Figure 12. The comparison of runtime between the GPU solver and CPU solver at different mesh sizes, red bars represent the GPU solver results and blue bars represent the CPU solver results.

significant with the increase of the mesh size. The reason is that Pethiyagoda [42] only introduced the GPU acceleration technique in the boundary integral method not the whole process, whereas this paper proposes a complete parallel computing framework including the parallel process for inverting preconditioner matrix. In the process of inverting, the larger mesh size the heavier computational load, hence the advantage of the GPU solver proposed in this paper can be more significant.

5.2.3. Capability

In the proposed GPU solver, like the CPU solver [40,41], three parameters can be used to regulate simulation results, namely Froude number, source strength and source type. Numerical simulations show that the wake angle is tending to decrease with Froude number but increase with the source strength, and that the divergent waves will be prominent with the sufficiently large Froude number or the Rankine source, and that the wavelength dramatically increases with Froude number. By adjusting these parameters, the wake characteristics including the wake angle, wave amplitude, prominent waves and wavelength can be changed, resulting in the polymorphic ship wave patterns. For example, the wake waves of high-speed boats can be generated by using large Froude number, low source strength and Rankine source. As the vessel speed decreases and overall length increases, it is appropriate to choose small Froude number and a higher strength Kelvin source. Comparisons between the three types of real ship waves and the corresponding GPU solver simulation results are shown in Figure 13. It is found that the simulation results are in good agreement with the real ship waves, the proposed GPU solver can also generate high quality simulation patterns for 3-D nonlinear ship wave.

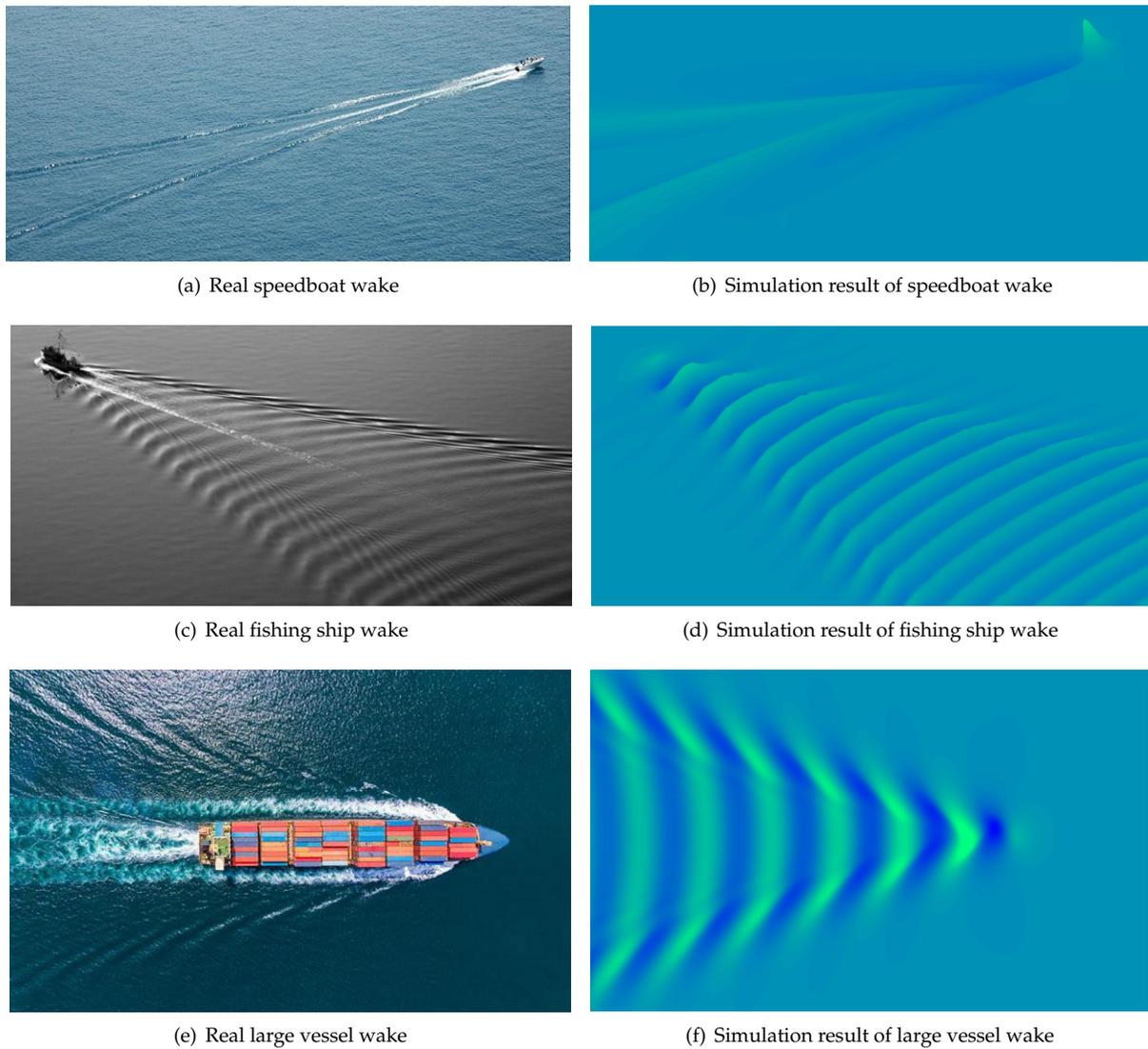


Figure 13. Comparisons between the three types of real ship waves and the corresponding the GPU solver simulation results. The picture of real speedboat wake pattern comes from internet <https://www.quanjing.com>, accessed on 1 september 2023; the picture of real fishing ship wake comes from <https://www.shutterstock.com>, accessed on 1 september 2023; the picture of real large vessel wake comes from internet <https://blogs.worldbank.org>, accessed on 6 september 2023.

6. Conclusions

The numerical simulation of ship wave is important for practical ocean engineering. This paper proposes a highly-paralleled numerical scheme for simulating three-dimensional (3-D) nonlinear Kelvin ship waves effectively, including a numerical model for nonlinear ship waves, a banded preconditioner JFNK method and a GPU based parallel computing framework. Numerical simulations show that the proposed GPU solver can save GPU memory and obtain high efficiency significantly. This highly-paralleled numerical scheme provides an opportunity for the further study of the nonlinear Kelvin ship waves on a large scale, the following conclusions can be drawn.

- (1) The bandwidth has an effect on the running memory and runtime of the GPU solver. Based on the mesh size, the value of the most appropriate bandwidth is around $\frac{M}{3} \times (N + 1)$, with more than 66% GPU memory can be saved.
- (2) The GPU solver can obtain an accurate numerical solution. The mean square error of GPU solver results and CPU solver results is $MSE=9.37E-8$, which is acceptable.

- (3) By designing the GPU parallel computing framework, the computation of ship wave simulation is accelerated up to 20 times. 430
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Although an highly-paralleled numerical scheme for nonlinear ship wave is proposed in this paper, some assumptions are still made in the construction of the numerical model, such as infinite water depth and the steady motion of ship on calm water. It is of great significance to improve simulation results by further exploring the influence of finite water depth, tangential flow and unsteady ship motion on nonlinear ship waves. 432
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Conflicts of Interest: The authors declare no conflict of interest. 445

Abbreviations 446

The following abbreviations are used in this manuscript: 447

JFNK	Jacobian-free Newton-Krylov	448
GMRES	Generalized Minimum Residual	
CUDA	Compute Unified Device Architecture	449
GPU	Graphics Process Unit	
CPU	Central Processing Unit	
MSE	Mean Square Error	

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